# Scaling properties of neural networks for job-shop scheduling 

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#### Abstract

This paper investigates the scaling properties of neural networks for solving job-shop scheduling problems. Specifically, the Tank-Hopfield linear programming network is modified to solve mixed integer linear programming with the addition of step-function amplifiers. Using a linear energy function, our approach avoids the traditional problems associated with most Hopfield networks using quadratic energy functions. Although our approach requires more hardware (in terms of processing elements and resistive interconnects) than a recent approach by Zhou et al. [2], the neurons in the modified Tank-Hopfield network do not perform extensive calculations unlike those described by Zhou et al.


Keywords: Optimization; Scheduling; Mixed integer linear programing; Tank-Hopfield neural networks

## 1. Introduction

Hopfield-type feedback neural networks have been successfully applied to solving a variety of interesting problems such as the traveling-salesman problem, analog-to-digital conversion [1], resource allocation, tiling problem, $k$-colorability problem, and many others as described by Takefuji [5]. Recent criticisms of the

[^0]Hopfield networks such as nonconvergence of the network to valid solutions, inability to locate the global minimum, and poor scaling properties, are due to the use of quadratic energy functions as pointed out by Zhou et al. [2]. This work is an extension of our earlier results reported in $[4,6]$ which uses a modified Tank-Hopfield linear programming network with a linear energy function to overcome the shortcomings of the traditional quadratic energy functions.

Job-shop scheduling belongs to the large class of $N P$-complete (nondeterministic polynomial time complete) problems. An NP-complete problem exhibits an exponential growth in the computation time as the size of the problem increases linearly, and is commonly referred to as a 'hard' problem. In general, an NP-complete problem is one which, for some input of size $N$, takes a time proportional to at least $2^{N}$. NP-complete problems occur naturally in many situations. For example, there are more than $N$ ! ways to schedule $N$ courses to various classrooms and instructors, which means that determining a complete schedule of a large number of classes pleasing to both students and professors can become a formidable task!

In general, the job-shop scheduling problem can be stated as follows: Given $n$ jobs that have to be processed on $m$ machines in a prescribed order under certain restrictive assumptions, what is then the optimal order in which each machine handles the jobs? In short, job-shop scheduling is a resource allocation problem. The resources are called machines and basic tasks are called jobs. Each job may consist of several subtasks referred to as operations that are interrelated by precedence restrictions. A close resemblance to the job-shop problem is processor scheduling in a multiprocessor computer system where the problem definition is: Given a deadline and a set of tasks of varying length to be performed on two identical processors, can the tasks be arranged so that the deadline is met?

In a general job-shop problem, each operation is described by a triplet ( $i, j, k$ ), i.e. operation $j$ of job $i$ is to be executed on machine $k$. Assuming there are $m$ machines, then each job has exactly $m$ operations with exactly one operation on each machine. If there are $n$ jobs, then each machine must perform $n$ operations, which means that the number of possible sequences is therefore $n$ ! for each machine. If the sequences on each machine are independent, there are ( $n!)^{m}$ schedules. However, since each job consists of several operations with a linear precedence structure, many of these schedules will have conflicts and therefore are invalid solutions.

An example 4-job 3-machine job-shop problem is shown by a Gantt chart in Fig. 1(a). Each job-operation-machine triplet ( $i, j, k$ ) is represented by a block; the length of block is equal to the processing time $t_{i j k}$ required to perform the operation while the numbers on the horizontal axis represent completion times. Note that the processing order of each job by all machines and processing time of each operation are assumed known and fixed (static). A feasible schedule is one where all operations of each job can be placed on one time axis in precedence order and without overlap. In principle, there are infinitely many feasible schedules for a given job-shop problem since an arbitrary amount of idle time can be inserted at any machine between pairs of operations. Superfluous idle time exists in a schedule if an operation can be processed earlier in time without altering the


Fig. 1. (a) An example 4-job 3-machine job-shop problem, (the horizontal axis denotes processing times); (b) a near optimum schedule produced by the simulator described in [6].
operation sequences on any machine. In a nondelay schedule, no machine is kept idle at a time when it could begin processing some operation. Although nondelay schedules can be expected to provide very good solutions, there is no guarantec that they provide the optimum solutions. A job-shop problem is completely solved if the starting times of all operations are determined, and the precedence relationships between the operations are not violated. In general, the optimality criteria for machine scheduling can be classified into various groups. These measures of performance include criteria based on completion-dates (i.e. the time at which the last job-operation is completed), flow-times (i.e. the amount of time the job spends in the shop), and makespan (i.e. the total elapsed time required to process all of a given set of jobs). In many cases, previous researchers have used minimization of makespan as the objective function. A near-optimum solution to the 4 -job 3 -machine problem is shown in Fig. 1(b) using the modified Tank-Hopfield network described in [6].

## 2. Problem formulation

Several integer linear programming formulations of the machine sequencing problem have been proposed in the past but no attempt has been made to identify and exploit any special structure of this integer linear programming technique. Recently, we [4] proposed a mixed integer linear approach to solving job-shop scheduling. In our approach, the determination of an optimal job-shop schedule can be formulated as a linear programming with integer adjustments to minimize the starting times of all jobs subject to a set of precedence constraints and restrictive assumptions.

Let $S_{i k}$ denote the starting time of job $i$ on machine $k$, and $t_{i j k}$ the processing time for operation ( $i, j, k$ ). Assuming operation ( $i, j-1, h$ ) precedes $(i, j, k$ ), then the inequalities representing precedence constraints in order for a set of $S_{i k}$ to be feasible are:

$$
\begin{equation*}
S_{i k}-S_{i h} \geq t_{i, j-1, h} \quad 1 \leq j \leq m, \quad 1 \leq i \leq n \tag{1}
\end{equation*}
$$

where $m$ and $n$ are the number of machines and number of jobs, respectively. The condition that all starting times must be positive results in the constraint:

$$
\begin{equation*}
S_{i k} \geq 0 \quad 1 \leq i \leq n \tag{2}
\end{equation*}
$$

It is also necessary to ensure that no two operations are processed simultaneously by the same machine at the same time. For example, if job $i$ precedes job $p$ on machine $k$ (i.e. operation ( $p, q, k$ ) starts after the completion of $(i, j, k)$ ), then

$$
S_{p k}-S_{i k} \geq t_{i j k}
$$

On the other hand, if job $p$ precedes job $i$ on machine $k$, it is also necessary that

$$
S_{i k}-S_{p k} \geq t_{p q k}
$$

Using a 'zero-one' variable $y_{i p k}$ to specify the operation sequence, i.e. $y_{i p k}=1$ if job $i$ precedes job $p$ on machine $k$, and $y_{i p k}=0$ otherwise, the above constraints become:

$$
\begin{align*}
& S_{p k}-S_{i k}+H *\left(1-y_{i p k}\right) \geq t_{i j k}  \tag{3}\\
& S_{i k}-S_{p k}+H * y_{i p k} \geq t_{p q k} \tag{4}
\end{align*}
$$

where constant $H$ represents an arbitrary positive number greater than the maximum value of all processing times $t_{i j k}$ 's such that the constraints (3) and (4) are satisfied.

Therefore, the entire job-shop problem formulation can be summarized as minimizing the cost function

$$
\sum_{i=1}^{n} S_{i k_{i}}
$$

subject to

$$
\begin{array}{lll}
S_{i k}-S_{i h} \geq t_{i, j-1, h} & 1 \leq j \leq m, & 1 \leq i \leq n \\
S_{p k}-S_{i k}+H *\left(1-y_{i p k}\right) \geq t_{i j k} & i \geq 1, p \leq n, & 1 \leq k \leq m \\
S_{i k}-S_{p k}+H * y_{i p k} \geq t_{p q k} & i \geq 1, p \leq n, & 1 \leq k \leq m \\
S_{i k} \geq 0 & 1 \leq i \leq n &
\end{array}
$$

where $k_{i}$ is the machine which the last operation of job $i$ is assigned. There are $m n$ constraints of type (1) or type (2), and $m n(n-1)$ constraints of type (3) or type (4), giving a total number of $m n^{2}$ constraints. There are also $m n$ number of $S_{i k}$ 's and $m n(n-1) / 2$ number of $y_{i p k}$ 's, resulting a total number of $m n(n+1) / 2$ variables. For example, in a 2 -job 3-machine problem, there are a total of 12 inequalities with 9 variables in the formulation, i.e.

$$
\begin{aligned}
& S_{11} \geq 0 \\
& S_{12}-S_{11} \geq t_{111} \\
& S_{13}-S_{12} \geq t_{122} \\
& S_{21}-S_{23} \geq t_{213} \\
& S_{22}-S_{21} \geq t_{221} \\
& S_{23} \geq 0 \\
& S_{21}-S_{11}+H *\left(1-y_{121}\right) \geq t_{111} \\
& S_{11}-S_{21}+H * y_{121} \geq t_{221} \\
& S_{22}-S_{12}+H *\left(1-y_{122}\right) \geq t_{122} \\
& S_{12}-S_{22}+H * y_{122} \geq t_{232} \\
& S_{23}-S_{13}+H *\left(1-y_{123}\right) \geq t_{133} \\
& S_{13}-S_{23}+H * y_{123} \geq t_{213}
\end{aligned}
$$

A feasible solution to the above set of contraint equations represents a valid (but not necessary optimal) solution to the job-shop problem.

## 3. Neural network architecture

The analog Tank and Hopfield linear programming network [1] seeks to minimize a cost function

$$
F(\mathbf{A}, \mathbf{V})=\mathbf{A} \cdot \mathbf{V}
$$

where $\mathbf{A}$ is row vector array of $N$ coefficients for $N$ variables which are components of column vector $\mathbf{V}$. This linear cost function is subject to a set of $M$ linear constraints among the $N$ variables:

$$
\mathbf{D}_{j} \cdot \mathbf{V} \geq \mathbf{B}_{j} \quad j=1, \ldots, M
$$

input currents


Fig. 2. The modified Tank and Hopfield network consisting of linear and nonlinear processors for solving job-shop scheduling problems, see [6].
where matrix $\mathbf{D}_{j}$ contain the $N$ variable coefficients of constraint equation $j$ and column vector $\mathbf{B}_{j}$ are the bounds.

Our proposed network for solving mixed integer-linear programming problems is based on a slightly modified Tank and Hopfield network, as shown in Fig. 2. The difference is the addition of nonlinear step-function $h$-amplifiers. The output voltage $V_{i}$ of linear $g$-amplifier represents the starting time of each operation after minimization, while output $Y_{i}$ of nonlinear $h$-amplifier represents the zero-one variable. The components of $\mathbf{A}$ are proportional to the input currents fed into the $g$ - and $h$-amplifiers. Each $g$ - and $h$-amplifier has an input time constant $\rho_{i} C_{i}$ which acts as a memory element. The descriptions of analog components for implementing the modified neural network can be found in [3] and [6].

The $M$ outputs ( $\psi_{j}$ ) of the $f$-amplifers represent constraint satisfaction. This set of amplifiers have a nonlinear transfer function

$$
\psi_{j}=f\left(U_{j}\right)= \begin{cases}0 & U_{j} \geq 0 \\ -U_{j} & U_{j}<0\end{cases}
$$

where

$$
\mathbf{U}_{j}=\mathbf{D}_{j} \cdot \mathbf{V}-\mathbf{B}_{j}
$$

i.e. if $U_{j}<0$ the output $\psi_{j}$ of the $f$-amplifier will be a large positive value indicating a violation of the $j$ th constraint. When this happens, a current of value $\left(\psi_{j} D_{j i}\right)$ will be fed back into the inputs of the $g$ - and $h$-amplifiers, causing the outputs $V_{i}$ to oscillate between + Vcc and $-V c c$, where Vcc is the power supply voltage of the operational amplifiers. If $U_{j} \geq 0$ the output $\psi_{j}$ of the $f$-amplifier is zero corresponding to a valid solution and no current will be fed back to the $g$ - and $h$-amplifiers. As a result, the output voltages of the $g$ - and $h$-amplifers will be stable and directly proportional to the input bias currents $A_{i}$ 's.

Since the currents generated by the $g$ - and $h$-amplifiers are summed at the inputs of the $f$-amplifiers, it is necessary that the time constants $\left(\rho_{i} C_{i}\right)$ at the inputs of $g$ - and $h$-amplifiers be matched. If we assume the response time of the $f$-amplifier is negligible compared to the $g$ - and $h$-amplifiers, then the dynamics of the $g$-amplifier can be described by:

$$
\begin{equation*}
C_{i} \frac{\mathrm{~d} U_{i}}{\mathrm{~d} t}=-A_{i}-\frac{U_{i}}{R_{i}}-\sum_{j}^{M} D_{j i} f\left(D_{j} \cdot V-B_{j}\right) \tag{5}
\end{equation*}
$$

where $M=m n^{2}$ (i.e. total number of constraints). Similarly, the differential equation describing the behavior of the $h$-amplifier is:

$$
\begin{equation*}
C_{i} \frac{\mathrm{~d} X_{i}}{\mathrm{~d} t}=-A_{i}-\frac{X_{i}}{R_{i}}-\sum_{j}^{M} D_{j i} f\left(D_{j} \cdot Y-B_{j}\right) \tag{6}
\end{equation*}
$$

Consider an energy function for the entire circuit of the form:

$$
\begin{aligned}
E= & \sum_{i}^{P} A_{i} V_{i}+\sum_{i}^{P} \frac{1}{R_{i}} \int_{0}^{V_{i}} g^{-1}(V) \mathrm{d} V+\sum_{i}^{P} \sum_{j}^{M} f f(z) \mathrm{d} z \\
& +\sum_{i}^{Q} \frac{1}{R_{i}} \int_{0}^{Y_{i}} h^{-1}(Y) \mathrm{d} Y+\sum_{i} \sum_{j}^{M} \int f(w) \mathrm{d} w
\end{aligned}
$$

where

$$
\begin{aligned}
& U=g^{-1}(V) \\
& X=h^{-1}(Y) \\
& z=D_{j} \cdot V-B_{j} \\
& w=D_{j} \cdot Y-B_{j} \\
& P=m n \quad \text { (i.e. total number of } \quad S_{i k} \text { 's) } \\
& Q=m n(n-1) / 2 \quad \text { (i.e. total number of } \quad y_{i p k} \text { 's) }
\end{aligned}
$$

It can be easily shown that $E$ is always a decreasing energy function which seeks out a minima in $E$ and stops. The proof is as follows:

$$
\begin{aligned}
\frac{\mathrm{d} E}{\mathrm{~d} t}= & \sum_{i}^{P} A_{i} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t}+\sum_{i}^{P} \frac{1}{R_{i}} J_{i} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t}+\sum_{i}^{P} \sum_{j}^{M} f(z) \frac{\mathrm{d} z}{\mathrm{~d} t} \\
& +\sum_{i}^{Q} \frac{1}{R_{i}} X_{i} \frac{\mathrm{~d} Y_{i}}{\mathrm{~d} t}+\sum_{i}^{Q} \sum_{j}^{M} f(w) \frac{\mathrm{d} w}{\mathrm{~d} t} \\
= & \sum_{i}^{P} A_{i} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t}+\sum_{i}^{P} \frac{U_{i}}{R_{i}} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t}+\sum_{i}^{P} \sum_{j}^{M} f\left(D_{j} \cdot V-B_{j}\right) D_{j i} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t} \\
& +\sum_{i}^{Q} \frac{X_{i}}{R_{i}} \frac{\mathrm{~d} Y_{i}}{\mathrm{~d} t}+\sum_{i}^{Q} \sum_{j}^{M} f\left(D_{j} \cdot Y-B_{j}\right) D_{j i} \frac{\mathrm{~d} Y_{i}}{\mathrm{~d} t} \\
= & \sum_{i}^{P} \frac{\mathrm{~d} V_{i}}{\mathrm{~d} t}\left\{-C_{i} \frac{\mathrm{~d} U_{i}}{\mathrm{~d} t}\right\}+\sum_{i}^{Q} \frac{\mathrm{~d} Y_{i}}{\mathrm{~d} t}\left\{-C_{i} \frac{\mathrm{~d} X_{i}}{\mathrm{~d} t}\right\} \\
= & -\sum_{i}^{P} C_{i} g^{-1^{\prime}}\left(V_{i}\right)\left(\frac{\mathrm{d} V_{i}}{\mathrm{~d} t}\right)^{2}-\sum_{i}^{Q} C_{i} h^{-1^{\prime}}\left(Y_{i}\right)\left(\frac{\mathrm{d} Y_{i}}{\mathrm{~d} t}\right)^{2}
\end{aligned}
$$

Since $C_{i}$ is positive, and $g^{-1^{\prime}}\left(V_{i}\right)$ and $h^{-1^{\prime}}\left(Y_{i}\right)$ are monotone increasing and step functions, respectively, it follows that $d E / d t \leq 0$ with $d E / d t=0$ if $d V_{i} / d t=$ $d Y_{i} / d t=0$ for all $i$. In other words, the time evolution of the network is a motion in state space which seeks out a minima in $E$ and stops.

## 4. Scaling properties

The total number of constraints is $m n^{2}$ which is also equal to the number of $f$-amplifiers required. The total number of variables is $m n(n+1) / 2$ which is also equal to the number of $g$ - and $h$-amplifiers. Fig. 3 shows the total number of constraints/variables required to solve a job-shop problem for a fixed $m=100$ and varying $n$ and a fixed $n=100$ and varying $m$, respectively. For a fixed $m=100$ machines problem, the number of constraints/variables show a polynomial growth for practical $n$ number of jobs. However, for a fixed $n=100$ jobs, a linear relationship exists between the number of constraints/variables and the number of machines, $m$.

The total number of amplifiers (neurons) required to implement the network is simply the sum of the number of constraints and variables in a given job-shop problem, as illustrated in Fig. 4. For a fixed $n=100$ jobs, the total number of amplifiers (i.e. $f, g$, and $h$ types) required grows linearly as the number of operations assigncd to the machines; while a fixed $m=100$ machines allocation shows an polynomial growth as the number of jobs increases.



Fig. 3. Graphs of total number of contraints and variables required for a $n$-job $m$-machine job-shop problem.


Fig. 4. Total number of amplifiers ( $f, g$, and $h$-types) required for a $n$-job $m$-machine problem.



Fig. 5. Total maximum number of interconnects required for a $n$-job $m$-machine problem.

The total maximum number of interconnects required is two times the product of the number of constraints and the number of variables, as observed from the architecture of Fig. 2. However, in practice, only a small fraction (about one-fourth) of the interconnect matrix is actually utilized (see [6]). Fig. 5 shows the maximum interconnects required for practical problems. Even though these numbers seem astronomical, recent and future advances in optical and VLSI technologies would make the hardware implementations feasible.

## 5. Discussions

We have shown how a modified Tank and Hopfield network can be utilized to solve difficult optimization problems such as the classical job-shop scheduling. Zhou et al. [2] also presented a modified Tank and Hopfield analog computational network which exhibits linear scaling property, and thus appears to be better than the original approach by Foo and Takefuji [4]. However, a closer look reveals that their approach requires extensive computations by each neural processor. Thus, there is less network complexity at the expense of more complex processing by each neuron. This contradicts the popular concept of neurons with simple activation functions. Our approach may require more neurons and interconnects, but each neuron has a very simple activation function. Thus, our network is self-contained and does not need extensive calculations as required in the approach by Zhou et al.

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## References

[1] D.W. Tank and J.J. Hopfield, Simple 'neural' optimization networks: An A/D Converter, signal decision circuit, and a linear programming circuit, IEEE Trans. Circuits Syst. CAS-33 (5) (May 1986) 533-541.
[2] D.N. Zhou, V. Cherkassky, T.R. Baldwin and D.E. Olson, A neural network approach to job-shop scheduling, IEEE Trans. Neural Networks 2 (1) (Jan. 1991) 175-179.
[3] S.Y. Foo, L.R. Anderson and Y. Takefuji, Analog components for the VLSI of neural networks, IEEE Circuits Devices 6, (4) (July 1990) 18-26.
[4] S.Y. Foo and Y. Takefuji, Integer-linear programming neural networks for job-shop scheduling, Proc IEEE IJCNN '88, San Diego, CA (1988) 341-348.
[5] Y. Takefuji, Neural Network Parallel Computing, (Kluwer, Dordrecht, 1992).
[6] S.Y. Foo, Y. Takefuji and H. Szu, Job-shop scheduling based on modified Tank-Hopfield linear programming networks Eng. Appl. Artificial Intelligence (3) (April/May 1994).


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